

FEATURES

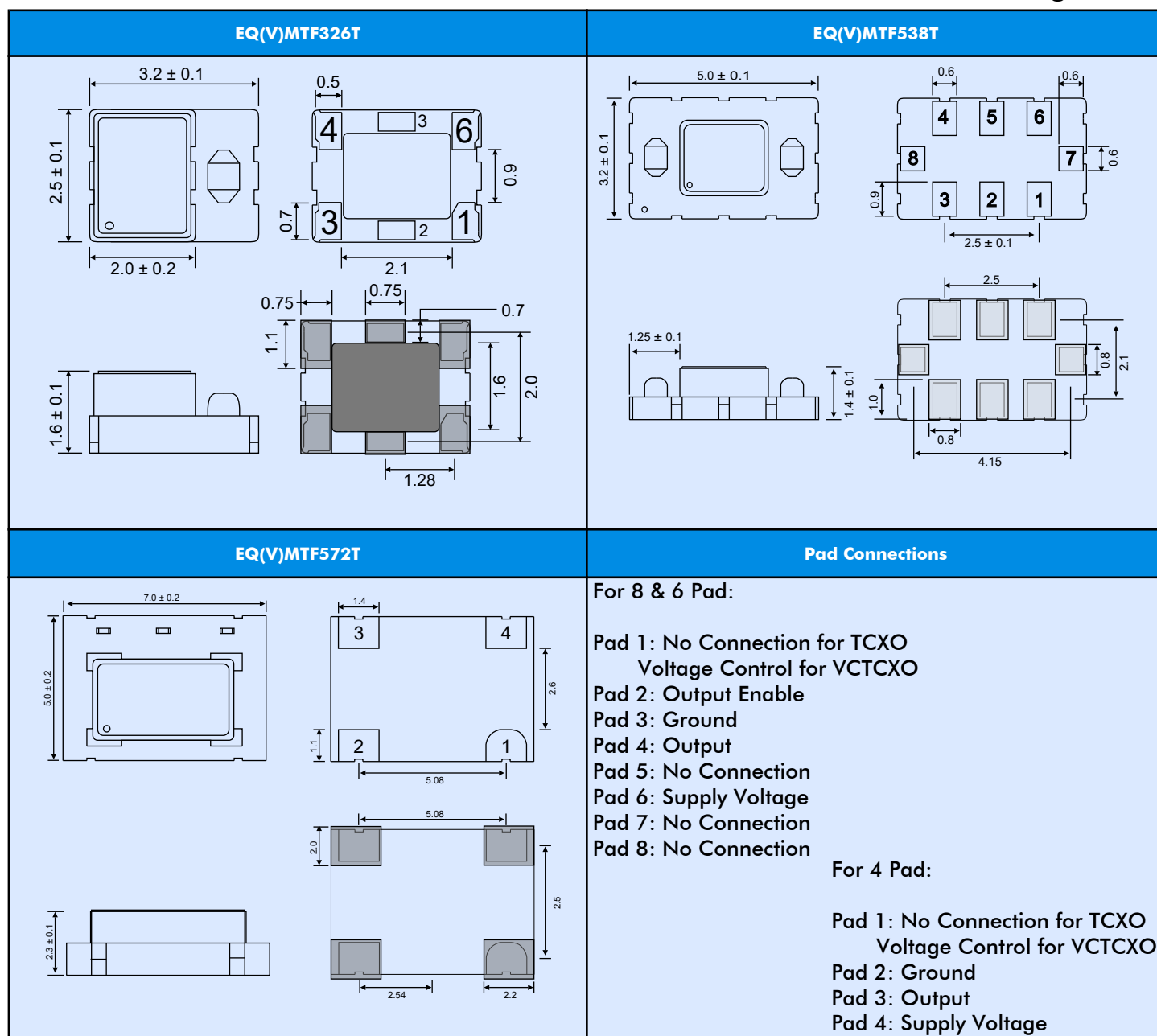
- Square Wave output
- Programmable CMOS
- ± 2.0 ppm over -40°C to $+85^{\circ}\text{C}$ Stability


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General Specifications at Ta = +25°C

Output Logic	CMOS					
Model	EQ(V)MTF326T (3.2 x 2.5 x 1.6 mm)	EQ(V)MTF538T (5.0 x 3.2 x 1.4 mm)			EQ(V)MTF572T (7.0 x 5.0 x 2.3mm)	
Supply Voltage (V _{DD})	+1.8V	+2.5V			+3.3V	
Frequency Range	1MHz (min.) 125MHz (max.)	1MHz (min.) 200MHz (max.)				
Current Consumption	30 mA	35 mA			40 mA	
Output Logic “High”, “1”	V _{dd} - 0.4V (min.)					
Output Logic “Low”, “0”	0.4V (max.)					
Output Load	15pF					
Rise / Fall Time	10.0 nsec (max.) (10% to 90% Waveform)					
Duty Cycle	1 ~ 150MHz: 50±5%					
	150 ~ 200MHz: 50±10%					
Start-up Time	5 msec. (Typ.), 10 msec (max.)					
RMS Jitter (typ.) (12KHz to 20MHz)	1.2 psec (typ.)					
Storage Temperature	-55°C to 150°C					
Initial Calibration Tolerance	± 2.0 ppm (max.) at +25°C ± 2°C					
Frequency Stability Codes	Temperature (ref to +25°C)	± 2.0 ppm (max.) over -40°C to +85°C (default)				
	Aging at Ta = +25°C	± 1.0 ppm (max.) Per year				
	Voltage Change	± 0.3 ppm (max.), For a ±5% input voltage change				
	Load Change	± 0.3 ppm (max.), For a ±10% load condition change				
	Reflow	± 1.0 ppm (max.), 1 reflow and measured 24 hours afterwards				
Phase Noise [dBc/Hz (typ.)]	Offset	10Hz	100Hz	1KHz	10KHz	100KHz
	100MHz	-72	-101	-155	-121	-119

Electrical Frequency Tuning by External Control Voltage			
Control Voltage Center	1.8V	2.5V	3.3V
	0.9V $\pm$ 0.6V	1.4V $\pm$ 1.0V	1.5V $\pm$ 1.0V
Frequency Deviation Range	± 5 ppm (min.)		
Slope Polarity	Positive Slope. Positive Voltage for Positive Frequency Shift		
Input Impedance	1.0M Ω (min.)		
Modulation Bandwidth	3KHz (min.)		
Linearity	$\pm 10\%$		
Output Enable / Disable Function [OE not available for EQ(V)MTF572T]	70% of V _{DD} (min.) To enable		
	30% of V _{DD} (max.) To disable		
	Disable Current: 0.2 mA (typ.), 10.0 mA (max.)		

**Part Number Format**

EQ(V)MTF_T part numbers are derived as follows:
Example: EQVMTF326T18-20.000-2.0/-40+85

Series Description

'EQMTF' = TCXO
'EQVMTF' = VCTCXO

Package Size

326 = 3.2x2.5mm
538 = 5.0x3.2mm
576 = 7.0x5.0mm

T = CMOS Output

Supply Voltage:

18 = 1.8V
25 = 2.5V
33 = 3.3V

Frequency**Frequency Stability Code****Operating Temperature****EQVMTF 326 T 18 20.000 2.0 -40+85**