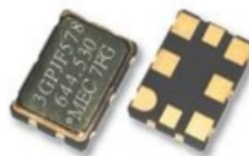


FEATURES

- Ultra Low RMS Jitter
- Available in 3.2 x 2.5mm, 5.0 x 3.2mm and 7.0 x 5.0mm packages
- Applications Include: High Resolution Audio & Video; High Speed Data Converters; Storage Area Networks

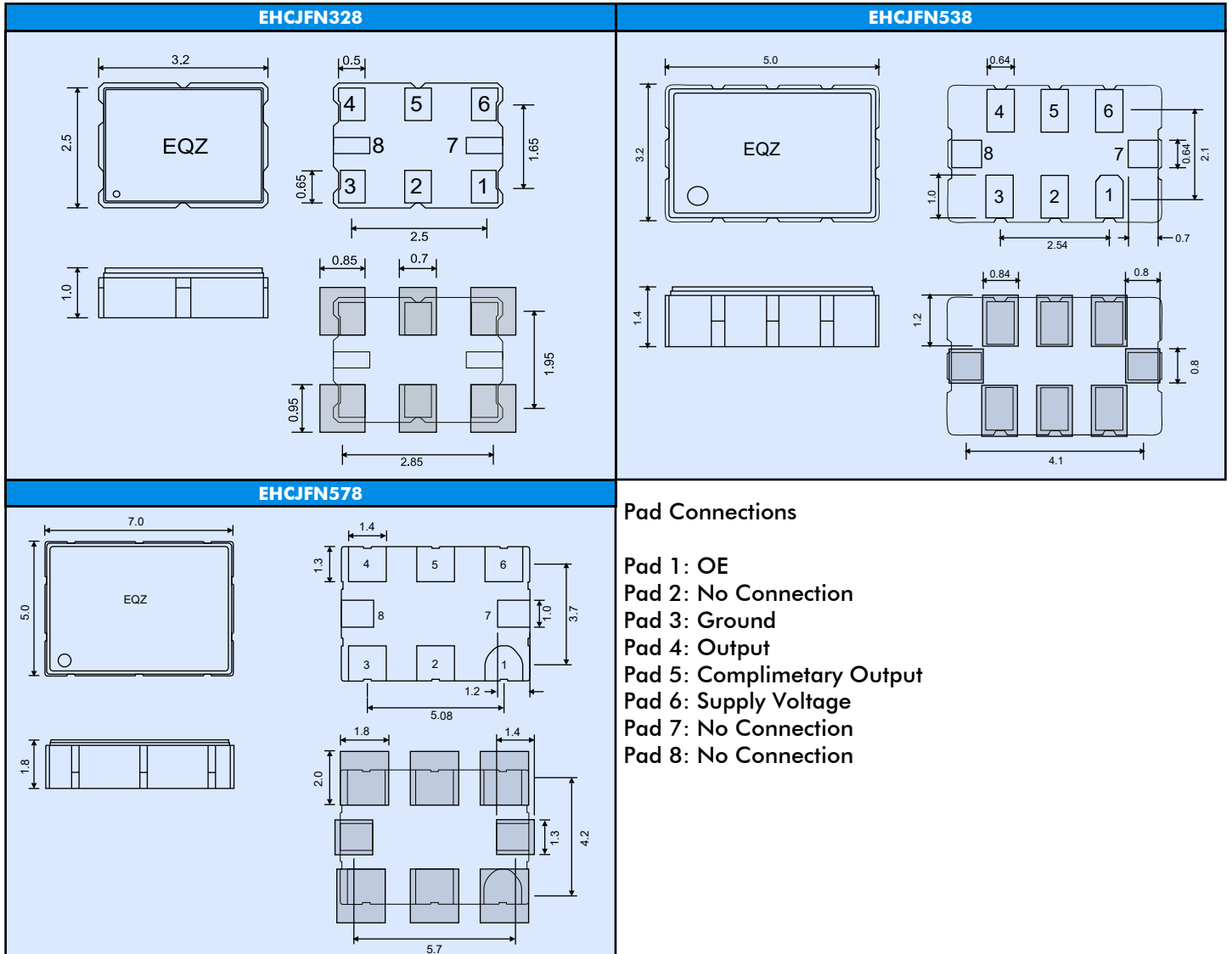

General Specifications at Ta = +25°C

Output Logic	HCSSL		
Model	EHCJFN		
Package Size (mm)	EHCJFN328 (3.2 x 2.5 x 1.4)	EHCJFN538 (5.0 x 3.2 x 1.4)	EHCJFN578 (7.0 x 5.0 x 1.8)
Supply Voltage (V _{DD})	1.8V ± 5%	+2.5V ± 10%	+3.3V ± 10%
Frequency Range	150MHz (min.) 700MHz (max.)		
Output Logic "High", "1"	V _{dd} : 0.66V (min.) V _{dd} : 1.15V (max.)		
Output Logic "Low", "0"	V _{dd} : -0.15V (min.) V _{dd} : 0.15V (max.)		
Output Voltage Swing	450mV (min.) 700mV (max.)		
Output Load	50Ω to ground		
Current Consumption (V _{dd} = +3.3V)	80 mA (typ.) 100 mA (max.)		
Disable Current	79 mA (typ.)		
Rise / Fall Time	0.4 nsec. (max.) (20% to 80% Waveform)		

Frequency Stability Code	Frequency Stability Over Operating Temperature Range	±25 ppm	±50 ppm	±100 ppm
	Commercial (-10°C to +70°C)	A	B	C
	Industrial (-40°C to +85°C)	D	E	F

Duty Cycle	50±5%
Start-up Time	5 nsec. (Typ.) ; 10 nsec. (Max.)
RMS Jitter (typ.) (12KHz to 20MHz)	156.250MHz: 159 fsec ; 491.520 MHz: 155 fsec ; 644.530 MHz: 151 fsec ; 2,000 MHz: 163 fsec
Storage Temperature	-55°C to 150°C
Aging at Ta = +25C	±3 ppm (max.) First year ; ±2 ppm (max.) per year thereafter
Enable / Disable Time	2.5 msec (max.) / 10usec. (max)

Outline Dimensions (in mm) and suggested pad layout



Pad Connections

- Pad 1: OE
- Pad 2: No Connection
- Pad 3: Ground
- Pad 4: Output
- Pad 5: Complimentary Output
- Pad 6: Supply Voltage
- Pad 7: No Connection
- Pad 8: No Connection

Part Number Format

EHCJFN part numbers are derived as follows:
 Example: 200.000/25EHCJFN5781E

