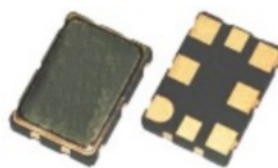


FEATURES

- Ultra Low RMS Jitter
- 3.2 x 2.5 mm, 5.0 x 3.2mm, and 7.0 x 5.0mm Package Sizes Available

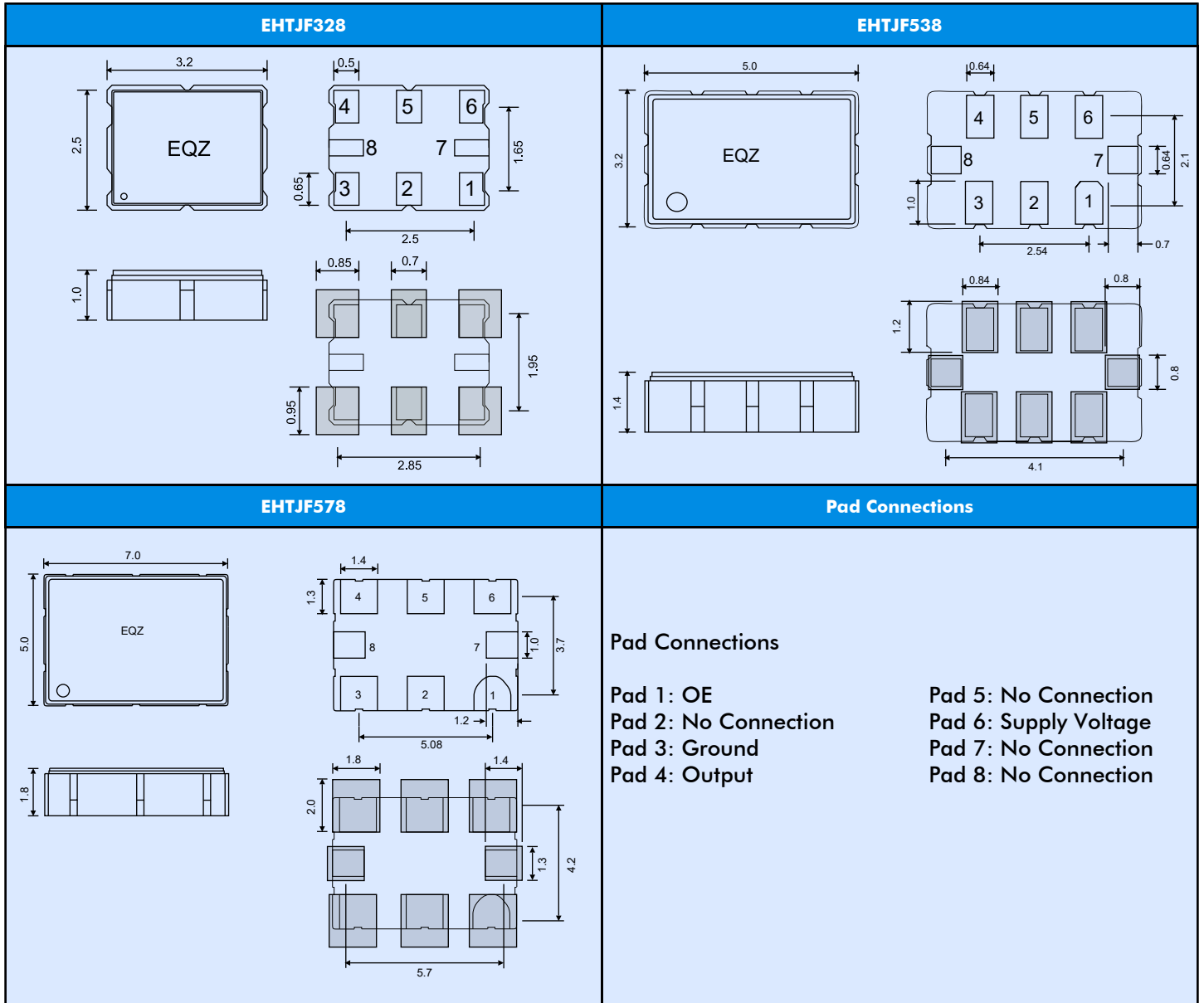


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General Specifications at Ta = +25°C, CL = 15pf

Output Logic	CMOS			
Model	EHTJF			
Package Size (mm)	EHTJF328 (3.2 x 2.5 x 1.0)	EHTJF538 (5.0 x 3.2 x 1.4)	EHTJF578 (7.0 x 5.0 x 1.8)	
Supply Voltage (V _{DD})	+1.8V ±5%	+2.5V ±10%	+3.3V ±10%	
Frequency Range	150MHz (min.)			
	250MHz (max.)			
Output Logic "High", "1"	90% V _{DD} (min.)			
Output Logic "Low", "0"	10% V _{DD} (max.)			
Output Load	15pF			
Current Consumption (V _{DD} = +3.3V)	75 mA (typ.)			
	90 mA (max.)			
Disable Current	62 mA (typ.)			
Rise / Fall Time	5.0 nsec. (Max.) (10% to 90% Waveform)			
Frequency Stability Code	Frequency Stability Over Operating Temperature Range	±25 ppm	±50 ppm	±100 ppm
	Commercial (-10C to +70C)	A	B	C
	Industrial (-40C to +85C)	D	E	F
Duty Cycle	50±5%			
Start-up Time	5 nsec. (Typ.) ; 10 nsec. (Max.)			
RMS Jitter (typ.) (12KHz to 20MHz)	156.250MHz: 150 fsec ; 491.520 MHz: 155 fsec ; 644.530 MHz: 151 fsec ; 2,000 MHz: 163 fsec			
Storage Temperature	-55C to 150C			
Aging at Ta = +25C	±3 ppm (max.) First year ; ±2 ppm (max.) per year thereafter			
Enable / Disable Function on Pad 1	80% of V _{DD} (min.) to enable output			
	20% of V _{DD} (max.) to disable output			
Enable / Disable Time	2.5 msec (max.) / 10usec. (max)			

Outline Dimensions (in mm) and suggested pad layout



Part Number Format

EHJF part numbers are derived as follows:
 Example: 125.000/25EHTJF578ET125.00

