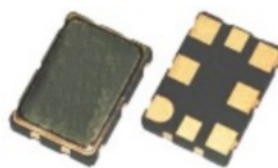


**FEATURES**

- Ultra Low RMS Jitter
- 3.2 x 2.5 mm, 5.0 x 3.2mm, and 7.0 x 5.0mm Package Sizes Available



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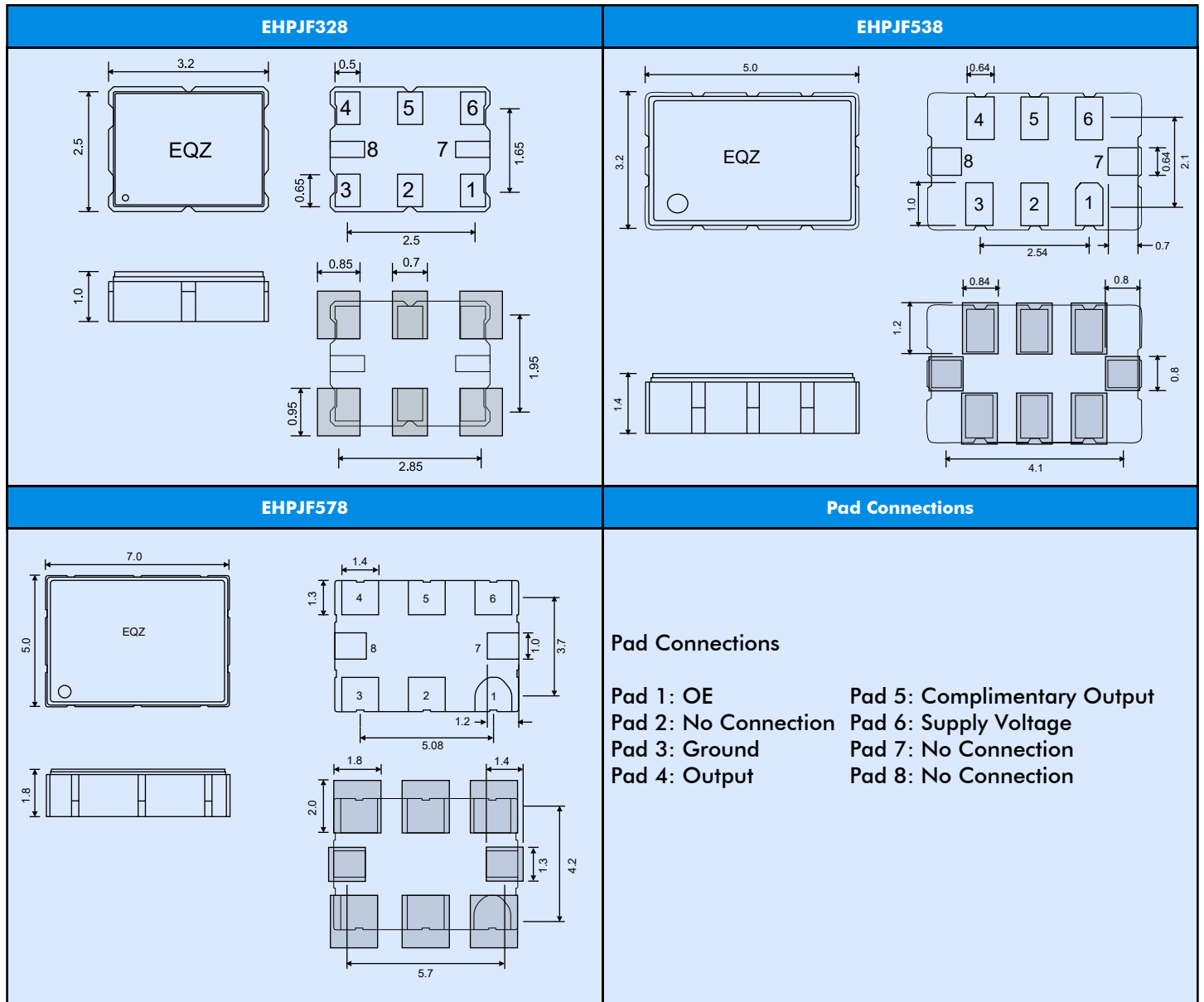

**General Specifications at Ta = +25°C**

Output Logic	PECL		
Model	EHPJF		
Package Size (mm)	EHPJF328 (3.2 x 2.5 x 1.0)	EHPJF538 (5.0 x 3.2 x 1.4)	EHPJF578 (7.0 x 5.0 x 1.8)
Supply Voltage (V <sub>DD</sub> )	+2.5V ±10%		+3.3V ±10%
Frequency Range	150MHz (min.) 2,100MHz (max.)		
Output Logic "High", "1"	V <sub>dd</sub> - 1.165V(min.) V <sub>dd</sub> - 0.8V (max.)		
Output Logic "Low", "0"	V <sub>dd</sub> - 2.0V (min.) V <sub>dd</sub> - 1.55V (max.)		
Output Load	50Ω into V <sub>dd</sub> -2V or Thevenin Equivalent		
Output Voltage Swing	595mV (min.) 930mV (max.)		
Current Consumption (V <sub>dd</sub> = +3.3V)	100 mA (typ.) 120 mA (max.)		
Disable Current	99 mA (typ.)		
Rise / Fall Time	4.0 nsec. (max.) (20% to 80% Waveform)		

Frequency Stability Code	Frequency Stability Over Operating Temperature Range	±25 ppm	±50 ppm	±100 ppm
	Commercial (-10°C to +70°C)	A	B	C
	Industrial (-40°C to +85°C)	D	E	F

Duty Cycle	50±5%
Start-up Time	5 nsec. (Typ.) ; 10 nsec. (Max.)
RMS Jitter (typ.) (12KHz to 20MHz)	156.250MHz: 150 fsec ; 491.520 MHz: 155 fsec ; 644.530 MHz: 151 fsec ; 2,000 MHz: 163 fsec
Storage Temperature	-55C to 150°C
Aging at Ta = +25C	±3 ppm (max.) First year ; ±2 ppm (max.) per year thereafter
Enable / Disable Function on Pad 1	80% of V <sub>dd</sub> (min.) to enable output 20% of V <sub>dd</sub> (max.) to disable output
Enable / Disable Time	2.5 msec (max.) / 10usec. (max)

Outline Dimensions (in mm) and suggested pad layout



**Part Number Format**

EHDJF part numbers are derived as follows:  
 Example: 125.000/25EHPJF578ET125.00

