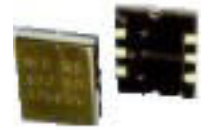


## 11.4 x 9.6 x 4.7mm 6 pad SMD VCXO

- Frequency range 750kHz to 800MHz
- LVPECL Output
- Supply Voltage 3.3 VDC
- Phase jitter 2.35ps typical
- Pull range from  $\pm 30$ ppm to  $\pm 150$ ppm



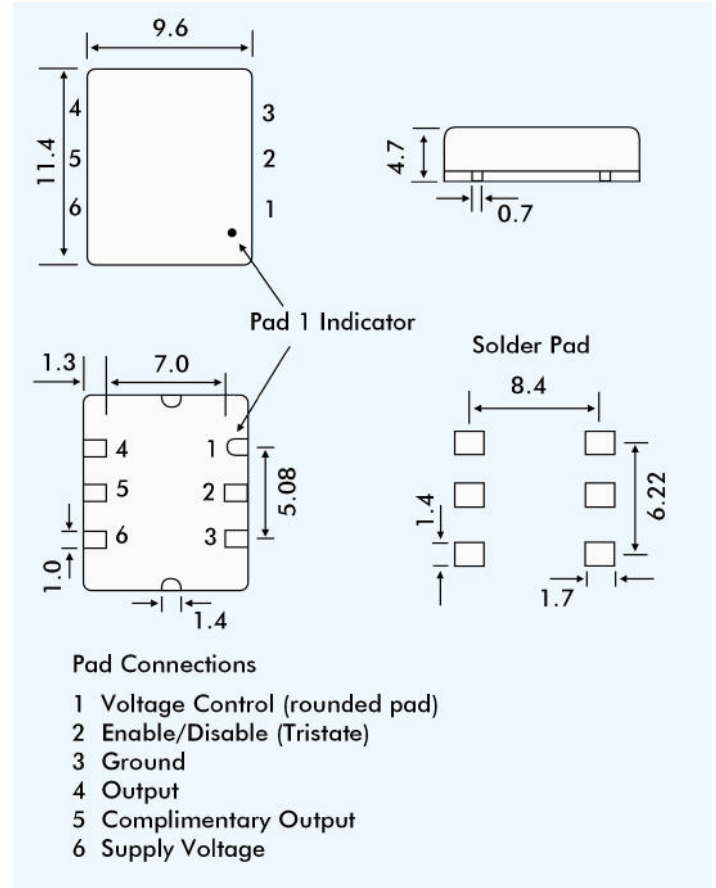
### DESCRIPTION

GPW64 VCXOs are packaged in a 6 pad 7mm x 5mm SMD package. Typical phase jitter for GPW series VCXOs is 2.35ps. Output is LVPECL. Applications include phase lock loop, SONET/ATM, set-top boxes, MPEG, audio/video modulation, video game consoles and HDTV.

### SPECIFICATION

Frequency Range:	750.0kHz to 800.0MHz
Supply Voltage:	3.3 VDC $\pm 5\%$
Output Logic:	LVPECL
RMS Period Jitter:	4.3ps typical
Peak to Peak Jitter:	27.0ps typical
Phase Jitter:	2.35ps typical
Initial Frequency Accuracy:	Tune to the nominal frequency with $V_c = 1.65 \pm 0.2$ VDC
Output Voltage HIGH (1):	$V_{dd} - 1.025$ V minimum $V_{dd} - 0.880$ V maximum
Output Voltage LOW (0):	$V_{dd} - 1.810$ V minimum $V_{dd} - 1.620$ V maximum ( $R_L = 50\Omega$ to $V_{dd} - 2V$ )
Pulling Range:	From $\pm 30$ ppm to $\pm 150$ ppm
Control Voltage Range:	$1.65 \pm 0.35$ Volts
Temperature Stability:	See table
Output Load:	$50\Omega$ into $V_{dd}$ or Thevenin equiv.
Rise/Fall Times:	0.5ns typ., 0.7ns max. 20% $V_{dd}$ to 80% $V_{dd}$
Duty Cycle:	50% $\pm 5\%$ (Measured at $V_{dd} - 1.3V$ )
Start-up Time:	10ms maximum, 5ms typical
Current Consumption:	75mA maximum at 212.5MHz 80mA maximum at 622.08MHz
Static Discharge Protection:	2kV maximum
Storage Temperature:	$-55^\circ$ to $+150^\circ$ C
Ageing:	$\pm 2$ ppm per year maximum
Enable/Disable:	See table
RoHS Status:	Fully compliant or non-compliant versions available

### OUTLINE & DIMENSIONS



### FREQUENCY STABILITY

Stability Code	Stability $\pm$ ppm	Temp. Range
A	25	$0^\circ \sim +70^\circ$ C
B	50	$0^\circ \sim +70^\circ$ C
C	100	$0^\circ \sim +70^\circ$ C
D	25	$-40^\circ \sim +85^\circ$ C
E	50	$-40^\circ \sim +85^\circ$ C
F	100	$-40^\circ \sim +85^\circ$ C

If non-standard frequency stability is required  
Use 'I' followed by stability, i.e. I20 for  $\pm 20$ ppm

### ENABLE/DISABLE FUNCTION

Tristate Pad Status	Output Status
Not connected	LVPECL and Complimentary LVPECL enabled
Below 0.3V <sub>dd</sub> (Ref. to ground)	Both outputs are disabled (high impedance)
Above 0.7V <sub>dd</sub> (Ref. to ground)	Both outputs are enabled

### PART NUMBERING

