

- Rugged construction for severe environments
- Electrically compensated for vibration
- Frequency Range: 50MHz - 100MHz
- Tight temperature stability, from  $\pm 2\text{ppm}$  over  $-40^\circ$  to  $+85^\circ\text{C}$
- Output Signal: CMOS Squarewave



### SPECIFICATIONS

Frequency Range:	50MHz to 100MHz
Output:	CMOS into 15pF load $\pm 10\%$
Symmetry:	50% $\pm 10\%$
Rise/Fall Time:	$\leq 10\text{ns}$
Frequency Stability:	$\pm 2 \times 10^{-6}$ over $-40^\circ$ to $+85^\circ\text{C}$ <i>Ramp rate of up to 2°/minute. Shall meet spec within 1 minute of turn on.</i>
Frequency vs. Supply:	$\pm 3 \times 10^{-7}$ for a 5% change
Ageing:	$< 1.0\text{ppm/year}$
Supply Voltage:	+3.3VDC or +5.0VDC $\pm 5\%$
Input Current:	30mA maximum
Phase Noise:	<i>See table</i>
Acceleration Sensitivity:	$\leq 7 \times 10^{-10}/\text{g}$ in the worst axis (SD option) $\leq 7 \times 10^{-11}/\text{g}$ in the worst axis (LG option)
Frequency Adjust:	$\pm 5.0\text{ppm}$ typical, positive slope 0V to Vsupply EFC

### PHASE NOISE

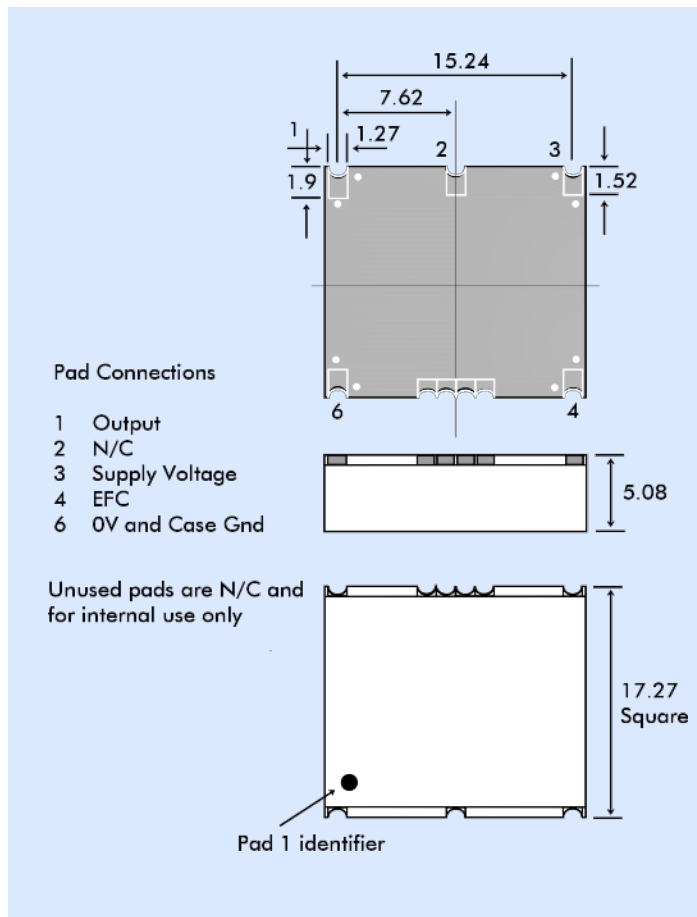
Offset	dBc/Hz
10Hz	-70
100Hz	-100
1kHz	-135
10kHz	-155
100kHz	-160

Floor = -165dBc/Hz

### ENVIRONMENTAL

Vibration:	per MIL-STD-202F, Meth. 214, Cond. I-F
Shock:	per MIL-STD-202F, Meth. 213, Cond K
Storage Temperature:	$-55^\circ$ to $+95^\circ\text{C}$

### T1241 - OUTLINES AND DIMENSIONS



### PART NUMBERING

Example: **T1241-3.3-LG-100.0MHz**

